

**AMENDMENTS TO THE CLAIMS**

What is claimed is:

1. (Currently Amended) A display device, comprising:

a display panel including a plurality of pixels provided in matrix in a first direction and in a second direction, the second direction intersecting with the first direction;

a driving section for sequentially driving, in the second direction, each pixel line provided along the first direction, the driving section causing the display panel to display an image that is in accordance with display data;

a reference voltage generating section for generating reference voltages that represent multiple gradations, the reference voltages being used for displaying the image in the multiple gradations;

a  $\gamma$ -correction adjustment section for adjusting the reference voltages so as to perform  $\gamma$ -correction of the display data;~~and~~

a memory section for separately storing  $\gamma$ -correction adjustment data and display data, the memory section being rewritable and nonvolatile; and

a control section for controlling the  $\gamma$ -correction adjustment section so as to change the reference voltages on which the  $\gamma$ -correction has been performed in accordance with the  $\gamma$ -correction adjustment data, supplied from the memory section via terminals different from terminals via which the display data is supplied, the control section decreasing display unevenness between pixels that are adjacent to one another in at least one of the first and the second directions.

2. (Currently Amended) A display device as set forth in claim 1, wherein:

the memory section is provided in the control section

~~the control section includes a memory for storing  $\gamma$ -correction adjustment data; and~~

~~the control section changes a  $\gamma$  correction value in accordance with the  $\gamma$  correction adjustment data.~~

3. (Currently Amended) A display device as set forth in claim 1, wherein:

the memory section is provided in the driving section

~~the driving section includes a memory for storing  $\gamma$  correction adjustment data; and~~

~~the control section changes a  $\gamma$  correction value in accordance with the  $\gamma$  correction adjustment data.~~

4. (Original) The display device as set forth in claim 1, wherein:

the display panel is divided into a plurality of display regions aligned in the first direction;

and

the driving section includes a plurality of drivers for driving the plurality of display regions respectively.

5. (Original) The display device as set forth in claim 1, wherein:

the reference voltage generating section includes a plurality of reference voltage generating circuits that are respectively for colors used for performing color display of the image.

6. (Original) The display device as set forth in claim 1, wherein:

the display panel includes a plurality of separate display panels provided in a surface direction of the display panel.

7. (Original) The display device as set forth in claim 1, wherein:

the display panel includes a plurality of small display panels that are bonded together so that display screens of the plurality of small display panels are on a same plane.

8. The display device as set forth in claim 1, wherein:

the display panel includes:

a thin-film transistor panel including (i) a plurality of pixel electrodes and (ii) thin-film transistors respectively for the plurality of pixel electrodes; and

an opposed panel on which opposed electrodes are provided; and

the thin-film transistor panel and the opposed panel are provided in an overlapping manner so that an electrode formation surface of the thin-film transistor panel and an electrode formation surface of the opposed panel face one another.

9. (Original) The display device as set forth in claim 1, wherein:

the display panel includes:

a plurality of thin-film transistor panels, each of which includes (i) a plurality of pixel electrodes and (ii) thin-film transistors respectively for the plurality of pixel electrodes; and

an opposed panel on which opposed electrodes are provided;

the plurality of thin-film transistor panels are bonded together so that display screens of the plurality of thin-film transistor panels are on a same plane; and

the plurality of thin-film transistor panels and the opposed panel are provided in an overlapping manner so that an electrode formation surface of the plurality of thin-film transistor panels and an electrode formation surface of the opposed panel face one another.

10. (New) The display device as set forth in claim 4, wherein:

the memory section is provided in the plurality of drivers.

11. (New) A display device, comprising:

display means including a plurality of pixels provided in matrix in a first direction and in a second direction, the second direction intersecting with the first direction;

driving means for sequentially driving, in the second direction, each pixel line provided along the first direction, the driving means causing the display panel to display an image that is in accordance with display data;

reference voltage generating means for generating reference voltages that represent multiple gradations, the reference voltages being used for displaying the image in the multiple gradations;

$\gamma$ -correction adjustment means for adjusting the reference voltages so as to perform  $\gamma$ -correction of the display data;

memory means for separately storing  $\gamma$ -correction adjustment data and display data, the memory means being rewritable and nonvolatile; and

control means for controlling the  $\gamma$ -correction adjustment means so as to change the reference voltages on which the  $\gamma$ -correction has been performed in accordance with the  $\gamma$ -correction adjustment data, supplied from the memory means via terminals different from terminals via which the display data is supplied, the control means decreasing display unevenness between pixels that are adjacent to one another in at least one of the first and the second directions.

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END OF CLAIM LISTING